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4,630,193	Dec. 16, 1986	Kris				
4,710,904	Dec. 1, 1987	Suzuki		_		
4,739,502	Apr. 19, 1988	Nozaki		1		
4,905,201	Feb. 27, 1990	Ohira et al.	_	_		
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RANGE XAMINER INITIALS	DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS	FILING DATE
<i>A</i> 4	4,330,852	May 18, 1982	Redwine et al.		_	
MA	4,703,418	Oct. 27, 1987	James	_	_	
M	4,785,394	Nov. 15, 1988	Fischer		_	
do	4,726,021	Feb. 16, 1988	Horiguchi et al.	_		
8N	4,870,562	Sept. 26, 1989	Kimoto et al.	_	_	
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EXAMINER INITIAL	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB CLASS	TRANSLATION YES/NO
M	S56-82961	July 7, 1981	Japan		1	YES
BA	S57-14922	Jan. 26, 1982	Japan			YES
M	Sho 60-80193	May 8, 1983	Japan		_	YES
M	Sho 60-55459	Mar. 30, 1985	Japan			YES
N.	S61-72350	April 14, 1986	Japan			YES
M	S63-142445	June 14, 1988	Japan			YES
M	B63-46864	Sept. 19, 1988	Japan			YES
ON	S64-29951	Jan. 31, 1989	Japan			YES
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AA			ry SRAMS"; IEEE International Solid S			266-267 (1987)
HA	Ohno, C.; "Self-Time	d RAM: STRAM";	Fujitsu Sci. TechJ., 24, 4, pp 293-300 (I	Dec. 1988)		
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M	4,205,373	May 27, 1980	Shah et al.	_	1	
A)	4,845,670	Jul. 4, 1989	Nishimoto et al.	_		
BN	4,509,142	Apr. 2, 1985	Childers		)	
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AN.	4,685,088	Aug. 4, 1987	Ianucci	_	-	

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MA	0 334 552	Mar. 16, 1989	EPO		(	
M	0 276 871	Jan. 29, 1988	EPO			

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M	European Search Report for EPO Patent Application No. 89 30 2613
SA	Z. Amitai, "New System Architectures for DRAM Control and Error Correction", Monolithic Memories Inc., Electro/87 and Mini/Mico Northeast: Focusing on the OEM Conference Record, pp. 1132, 4/31-3, (April 1987)
BA	N. Siddique, "100-MHz DRAM Controller Sparks Multiprocessor Designs", Electronic Design, pp. 138-141, (Sept 1986)
199	H. Kuriyama et al., "A 4-Mbit CMOS SRAM WITH 8-NS SERIAL ACCESS TIME", IEEE Symposium On VLSI Circuits Digest Of Technical Papers, pp. 51-52, (June 1990)
HI	J. Chun et al., "A 1.2ns GaAs 4K Read Only Memory", IEEE Gallium Arsenide Integrated Circuit Symposium Technical Digest, pp. 83-86, (Nov. 1988)
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**SERIAL NUMBER** 09/510,213

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ØN	4,754,433	06/28/88	Chin et al.		-	
M	5,023,488	06/11/91	Gunning		_	
gM	4,920,486	04/24/90	Nielson		_	
PM	4,719,602	01/12/88	Haq et al.	_		
M	4,263,650	04/21/81	Bennet et al.		_	
est .	3,771,145	11/06/73	Wiener	_	_	
8M	3,691,534	09/12/72	Varadi et al	_	_	
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		.'	U.S	. PATENT DOCUMENTS				
	XAMINER INITIALS	DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS	FILING DATE	
13/2	IK	4,766,536	G (07)23/88	Wilson, Jr. et al.		_		
$\sqrt{\Box}$	M	4,998,262	03/05/91	Wiggers		-		
	M	4,747,079	5 124/88	Yamaguchi		_		
"	On	4,649,511	03/10/87	Gdula	_	_		
	MM	4,757,473	07/12/88	Kurihara et al.	_			
	M	4,792,926	12/20/88	Roberts	_	_		
	MA	4,811,202	03/07/89	Schabowski	_	_		
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M	4,445,204	04/24/84	Nishiguchi		1	
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MA	4,951,251	08/21/90	Yamaguchi et al.	_		
AM.	4,928,265	12/29/92 05/1990	Beigho et al. Higuch	i et al.		
IM .	5,107,465	04/21/92	Fung et al.	_	)	
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M	4,953,128	08/28/90	Kawai et al.			
900	5,140,688	08/18/92	White et al.		(	
PAO	5,018,111	05/21/91	Madland			
SAN	4,734,880	03/29/88	Collins	_	-	
BO	4,183,095	01/08/80	Ward		_	
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MX	L. R. Metzeger, "A 16 pp. 562-567 (Oct. 198		rith Polysilicon Fusible Links", IEEE Jou	urnal of Solid Sta	te Circuits, v	ol. 18 No. 5,

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FARMWALD ET AL.

INFORMATION DISCLOSURE STATEMENT

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FILING DATE **FEBRUARY 22, 2000**  GROUP ART UNIT 2781

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M	5,109,498	04/28/92	Kamiya et al.	(	1	
SH	4,807,189	02/21/89	Pinkham et al.	-	(	:
MA	4,092,665	05/30/78	Saran	_	1	
Å	4,799,199	01/17/89	Scales, III et al.	(	1	
) RM	5,142,637	8 09/25/92	Harlin et al.			
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M	4,954,987	09/04/90	Auvinen et al.	_	_	
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M	4,788,667	11/29/88	Nakano et al.			
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Ph	5,301,278	04/05/94	Bowater et al.			
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